



Product information

Multilayer

1. Introduction

With this product information, we would like to supply you with basic details about the structural options, tolerances, materials and layout guidelines for multilayer boards. This should make your life easier as a developer and help to design your printed circuit boards so that they are optimised for manufacturing at lowest cost.

2. General details

	Standard	Special
Maximum circuit size	459 x 264	---
Number of layers	to 24	On request
Pressed thickness	0.5 mm – 3.2 mm	On request

3. Materials

As a supplier of prototypes and rush deliveries, **contag** has a selection of standard materials with which a large bandwidth of the variety of types of PCB can be covered and which are always available in house.

Requirements for other or for special materials can also be met in most cases, but, depending upon the exact requirements, up to about 5 working days may be needed to procure the material.

Do get in touch with us and discuss your needs with one of our sales or CAM team.

Standard materials held in stock:

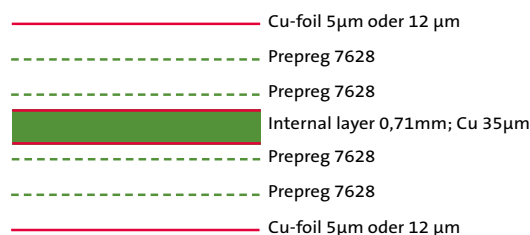
Material type	T _g	ε _r at 1 MHz	Components	Thickness	Tolerance	Fabric type	Cu thickness	
							Standard	on request
FR 4	Approx. 130°C	≤5,4 typically: 4,7	Internal layers	0.10mm	±0.018mm	1x2125	35µm	18/70µm
				0.15mm	±0.020mm	1x2157	35µm	18/70µm
				0.20mm	±0.025mm	2x2125	35µm	18/70µm
				0.25mm	±0.030mm	2x2165	35µm	18/70µm
				0.30mm	±0.038mm	2x2165	35µm	18/70µm
				0.36mm	±0.038mm	2x7628	35µm	18/70µm
				0.41mm	±0.040mm	2x2628	35µm	18/70µm
				0.51mm	±0.040mm	3x7628	35µm	18/70µm
				0.61mm	±0.045mm	3x7628	35µm	18/70µm
				0.71mm	±0.050mm	4x7628	35µm	18/70µm
			Prepregs	0.05mm*	Dep. on lay.	106	---	---
				0.06mm*	Dep. on lay.	1080	---	---
				0.10mm*	Dep. on lay.	2116	---	---
				0.18mm*	Dep. on lay.	7628	---	---

*theoretical pressed thickness

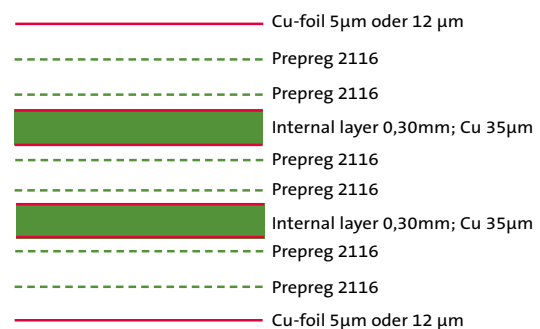
4. Multilayer options

If you do not specify a particular sequence of layers, we will use the following standard sequence for the corresponding number of layers:

Standard structure 4 layer multilayer, pressed thickness approx. 1.55 mm

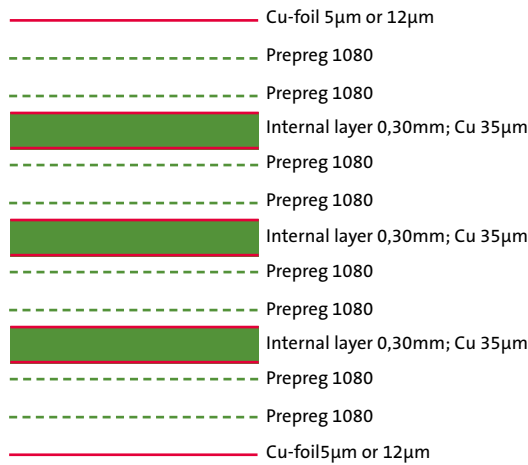


Standard structure 6 layer multilayer, pressed thickness approx. 1.50 mm

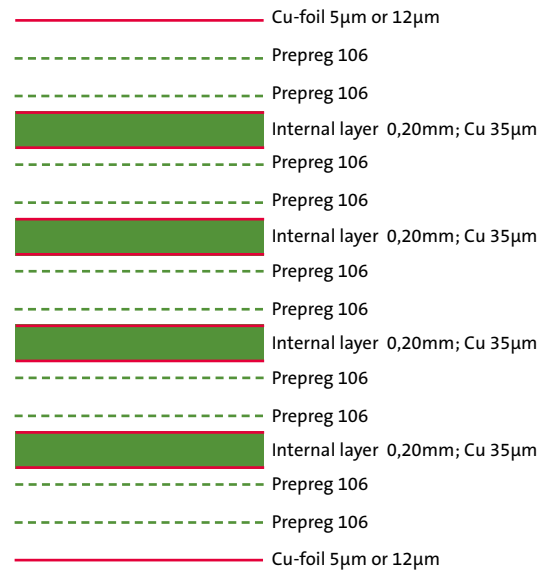




Standard structure 8 layer multilayer, pressed thickness approx. 1.60 mm



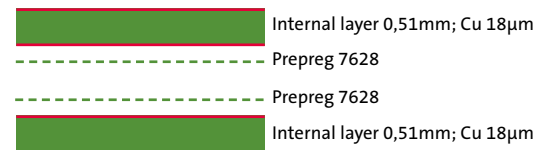
Standard structure 10 layer multilayer, pressed thickness approx. 1.60 mm



5. Multilayer laminations

If you would like a defined thickness of dielectric between the external layers and the first internal layer, then you can choose this type of construction. For this technology, there may be restrictions on the design of the layout for the outer layers as well as on the availability of the material. Discuss your needs with our sales department. They will provide you with reliable and competent advice.

Example of a 4 layer multilayer board using lamination



6. Multilayer boards with blind and buried vias

contag can produce multilayer circuit boards with blind and buried vias, even as a rush service. You will find important information and layout hints in our [technology information sheet "Blind vias"](#).

7. Design notes

When special layers are requested, we have a simulation programme, with which we can determine the exact insulation spacing, the degree of resin fill and the ϵ_r value between the individual layers. It is also possible to precisely calculate the final thickness. To do this we need the complete layout for the internal layers.

Basically the following, generally applicable points should be considered for the layers in your boards:

- Plan at least 2 prepreg mats between the layers. (otherwise the resin fill and the insulation may become critical)
- Build up multilayer boards symmetrically. (both for the thickness of the internal layers if you would like to use different core thicknesses, as well as for the prepregs)
- Avoid uneven distribution of Cu on an internal layer. (danger of twisting or warping)
- Note aspect ratio of $\geq 1:8$. (ratio of smallest drill diameter to pressed thickness)
- The width of pad left after drilling should be at least 0.125 mm around the full circumference. Clearances should be at least 0.30 mm larger than the associated hole diameter. (holes for component leads will be drilled 0.15 mm larger, vias 0.05 mm larger than the final diameter that you specify.)
- Put impedance-controlled tracks on internal layers. (Owing to the close tolerance of the Cu thickness, the cross-section of the tracks is thus more exactly reproducible, the same applies for the restricted tolerance of the thickness of internal layers affecting the dielectric thickness. For dielectric spacing with prepregs, tolerances of $\pm 10\%$ should be used for calculations.)

For further technological questions concerning circuit boards, please contact our team of technologists (Tel. 030/351 788 – 155).

** on request